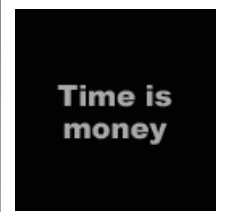


## 9P946AFLF

**Category:** Zero Delay Buffers  
**Generic Part:** 9P946  
**Market Group:** PC CLOCK  
**Description:** PC BUFFER  
 Dual DDR zero delay buffer

### Product Description/Features:

- High performance, low jitter zero delay buffer
- I2C for functional and output control
- Dual bank 1-6 differential clock distribution
- 2 separate feedback in & out for input to output synchronization for each bank
- Supports up to 4 DDR DIMMs
- Supports up to 533MHz (DDR II 1066)



### Parameters

Temperature	C
Voltage	3.3 V
Package	SSOP48
Speed	NA
Status	Active
Sample	No
Minimum Order Quantity	120
Factory Order Increment	30

### Distributor Inventory

No Pricing information is available from our Distributors at this time.

### Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

### Package

Package	SSOP 48 (PVG48)
Description	SSOP 300 MIL
Status	Active
Class	PLASTIC
Category	Green
Pb (Lead) Free	Yes
Mark	F
Package Type	SSOP
Lead Count	48
Length	15.9 mm
Width	7.5 mm
Thickness	2.3 mm
Pitch	0.64 mm
Tube / Tray	TUBE
Quantity per Tube/Tray	30
Quantity per Reel	1000
Moisture Sensitivity Level (MSL)	1
Moisture Exposure Floor Life	Unlimited@<30C/85% RH
Peak Reflow Temperature	260Å°C
Rebake Conditions	NA
Related Package Documents	<a href="#">view documents</a>